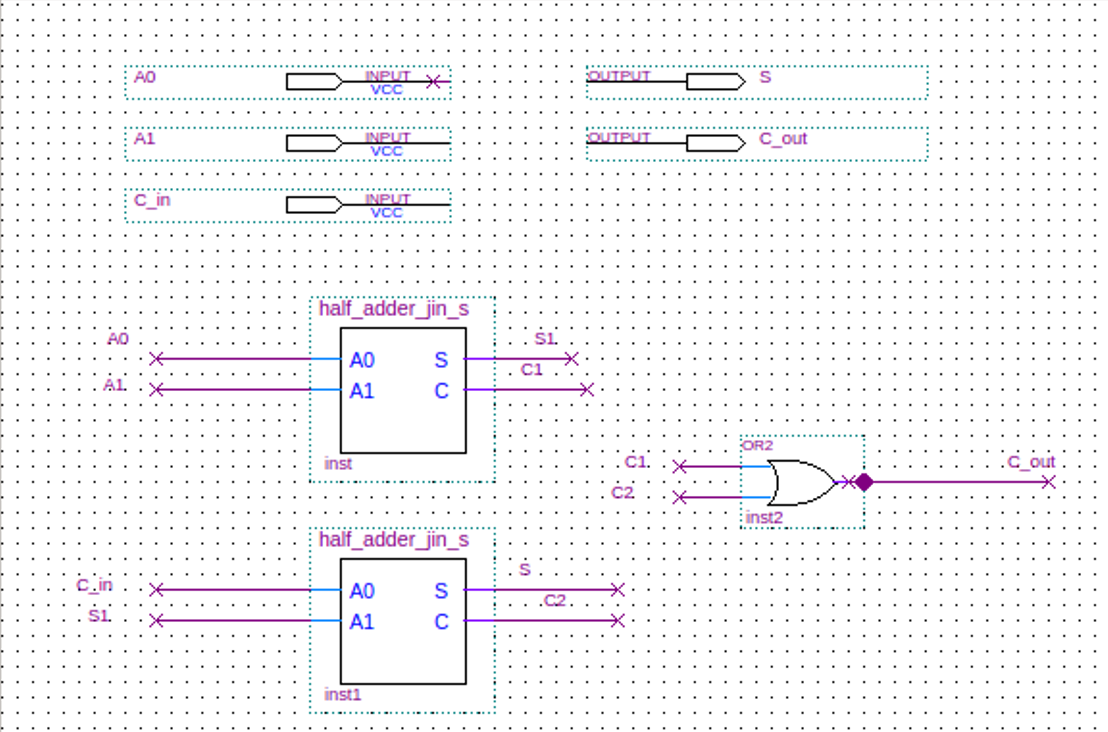
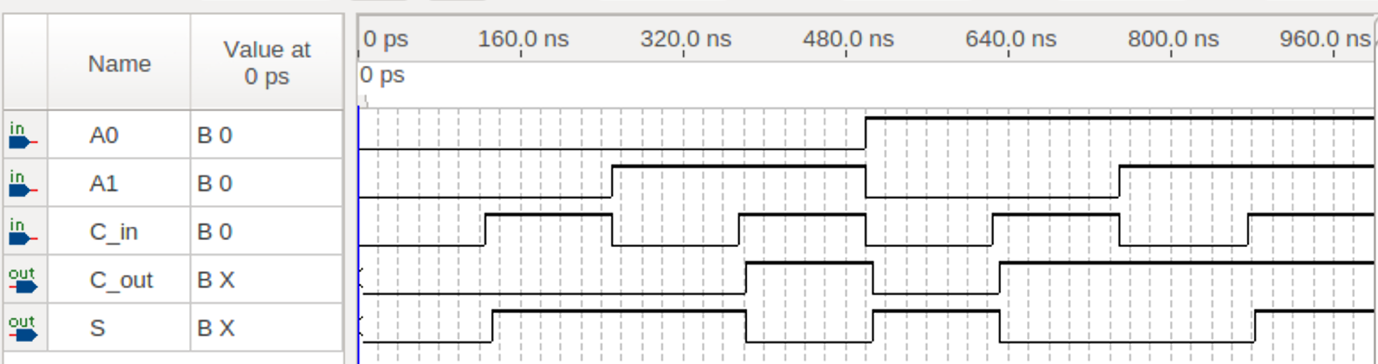
HW#7 디지털 회로 설계 및 언어 2015104027 박정진

1. 회로도



2. vwf



3. 폴더 hierarchy

